# **Evalueserve**

# <u> Internship Program – 2013 Batch</u>

Company	:	Evalueserve
Eligibility	:	B.Tech EC / ET (2013 Batch)
Internship period	:	2 Months
Stipend	:	INR 10,000/month
No. of vacancies	:	10

## Job Profile

Evalueserve is a global specialist in knowledge processes with a team of more than 2,600 professionals worldwide. As a trusted partner, Evalueserve analyzes, improves and executes knowledge-intensive processes and leverages its proprietary technology to increase efficiency and effectiveness. We have dedicated on-site teams and scalable global knowledge centers in Chile, China, India and Romania, which provide multi-time zone and multi-lingual services.

#### **Evalueserve Intellectual Property**

Patents are crucial in the business of technology because they define the ownership of specific technological advancements and innovations. Consequently, hi-tech companies need to have sound patent strategies involving continuous research and analysis. Researching and analyzing patents requires specific technology understanding and an analytical bent of mind. Accordingly, engineering and science students are best suited for critical analysis of patents. Evalueserve Intellectual Property team hires engineers and scientists for patent analysis work that supports various strategic initiatives of our clients from around the world.

#### **Position Description:**

Department	Intellectual Property
Type of Job	Internship – Two months
Location	Gurgaon, India
Job Title	Intern
Reporting to	Group Manager

#### **Job Description**

The candidate would be expected to perform patent analytics work relating to patent litigation, licensing, acquisition, standard mapping, competitive benchmarking, innovation tracking and much more. The candidate will be expected to analyze patents from a technological perspective and prepare reports to present the analysis.

Top performing interns will be offered full-time employment at the end of the internship.

#### **Training Opportunities**

- Interns will be trained on patent analytics work.
- Interns will also go through training sessions to develop verbal communication skills.

### **Technology Domains**

Telecommunications, semiconductors, VLSI, and other domains relevant to electronics

#### **Skill sets required**

- Strong technical knowledge and analytical skills
- Good written and spoken English communication
- Responsible attitude and ability to complete tasks in a timely manner
  - Interview Date, Time & Venue will be communicated to all registered students through email.

### How to Apply?

- 1. Interested & Eligible students need to click the below mentioned link and apply online. https://docs.google.com/spreadsheet/viewform?fromEmail=true&formkey=dFRoXzdWMDA2LXQ3aFE5Yk1HRmdhLVE6MA
- Once applied students have to appear in the Interview process or else his / her name will be blacklisted and no further opportunities will be provided.
- 3. Last date to apply for this proves is Thursday, 13<sup>th</sup> June 2013 by 4:00 pm
- 4. Entries received after the mentioned date will not be entertained.

Best of Luck

Prof. (Dr.) Ajay Rana Director